



Patent  
Attorney's Docket No. 032481-034

2122 #5  
Pre  
Amend  
JC  
4/3/02

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of )

Mukesh K. PATEL, et al. )

Application No.: 09/938,886 )

Filed: August 24, 2001 )

For: JAVA VIRTUAL MACHINE HARDWARE )  
FOR RISC AND CISC PROCESSORS )

Examiner: [prior application:  
C. Das]

Group Art Unit: [prior application:  
2122]

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MAR 29 2002

Technology Center 2100

**SECOND SUPPLEMENTAL PRELIMINARY AMENDMENT**

Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

Prior to examination of the application wherein a request for continuation application was filed together with a Preliminary Amendment, and to the Supplemental Preliminary Amendment filed therewith, please amend the application as follows:

**In the Claims:**

Please add new claims 276 - 357 as follows.

276. A system comprising:  
a first unit adapted to execute register-based instructions, the first unit having an associated register file;  
a hardware unit associated with the first unit, the hardware unit adapted to convert stack-based instructions into register-based instructions, the hardware unit adapted to store portions of the operand stack in the first unit's register file, the portions of the operand stack maintained using overflow and/or underflow indications; and

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01 FC:202  
02 FC:203

210.00 DP  
873.00 OP



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